

TCXO HIGH STABILITY 105 °C HIGH TEMPERATURE





Product Number

TG7050CKN: X1G005661xxxx14 TG7050SKN: X1G005671xxxx14 TG7050CMN: X1G005681xxxx14 TG7050SMN: X1G005691xxxx14

TG7050CKN / TG7050SKN TG7050CMN / TG7050SMN

• Frequency range : 10 MHz to 54 MHz Supply voltage 3.3 V Typ. • Frequency / temperature characteristics

: $\pm 0.1 \times 10^{-6}$ Max. (-40 °C to +105 °C)

• Free-run accuracy : ±4.6× 10⁻⁶ Max. / 20 years (for Stratum3) • External dimensions: 7.0 × 5.0 × 1.5 mm (10 pins or 4 pins) Applications Network synchronization, BTS, Microwave,

Stratum3, SyncE, IEEE1588 Features 105 °C High temp, High stability







TG7050CMN TG7050SMN (4 pins)

Specifications (characteristics)

Item	Symbol	CMOS	Clipped sine wave	Condition
Output frequency range	fo	10 MHz to 54 MHz		Please contact us about available frequencies.
Supply voltage	V _{cc}	3.3 V ±5 %		
Storage temperature	T_stg	-40 °C to +105 °C		Storage as single product.
Operating temperature	T use	-40 °C to +105 °C		
a) Frequency tolerance	f_tol	±1.0 × 1	0 ⁻⁶ Max.	After reflow, +25 °C
b) Frequency/temperature characteristics	fo-Tc	±0.1 × 10 ⁻⁶ Max.		-40 °C to +105 °C
c) Frequency/load coefficient	fo-Load	±0.1 × 10⁻⁶ Max.		Load ±10 %
d) Frequency/voltage coefficient	fo-V _{CC}	±0.1 × 1	0-6 Max.	V _{CC} ± 5 %
e) Frequency aging	£	±0.5 × 10 ⁻⁶ Max.		+25 °C, First year
	f_age	±3.0 ×10 ⁻⁶ Max.		+25 °C, 20 years
Holdover stability		±0.01 × 10 ⁻⁶ Max. ((+25 °C, 24 hours)	After 10 days of continuous operation
(Constant temperature)	-	±0.04 × 10 ⁻⁶ Max. (+25 °C, 24 hours)	After 48 hours of continuous operation
Wander generation (MTIE, TDEV)		Compliant with GR-1244CORE, ITU-T G.8262		
Free-run accuracy	-	±4.6 × 10 ⁻⁶ Ma	ax. / 20 years	This includes Item a), b), c), d) and e)
	Icc	7.0 mA Max.		10 MHz≤fo≤26 MHz
Current consumption		9.0 mA Max.	6.0 mA Max.	26 MHz < fo ≤ 40 MHz
		10.0 mA Max.		40 MHz < fo ≤ 54 MHz
Symmetry	SYM	45 % to 55 %	-	GND level (DC cut)
Output voltage	V_{OH}	90 % V _{CC} Min.	-	
	V_{OL}	10 % V _{CC} Max.	-	
Rise time / Fall time	tr/tf	8.0 ns Max.	-	10 % Vcc to 90 % Vcc level, Load:15 pF
Start-up time	t_str	5 ms. Max.		T = 0 at 90 % V _{CC}
Output level	Vpp	-	0.8 V Min.	Peak to Peak
Output load condition	Load	15 pF	10 kΩ//10 pF	
Input voltage	V_{IH}	70 % V _{CC} Min.		OE terminal (Enable voltage)
	V_{IL}	30 % V _{CC} Max.		OE terminal (Disable voltage)

^{*} Note: Please contact us for requirements not listed in this specification.

Product Name (Standard form)

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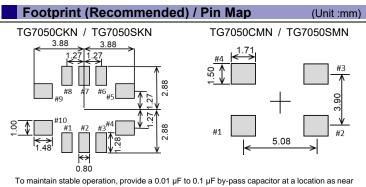
(5)

TG7050 C K N 30.720000MHz C A H H G A 6 7 8 9 10

①Model ②Output (C: CMOS, S: Clipped sine wave) ③Package type (K: 10 pins, M: 4 pins) ④Frequency ⑤Supply voltage (C: 3.3 V Typ.)

⑥Frequency/temperature characteristics (A: ±0.1 × 10⁻⁶ Max.) ⑦Operating temperature (H: -40 °C to +105 °C) ®OE function (H: Active High, N: Non) 9V_C function (G: V_C Non) 9Internal identification code ("A" is default)

External dimensions (Unit:mm) TG7050CKN / TG7050SKN TG7050CMN / TG7050SMN #8 #7 #6 #1 #2 #3 7.00 ± 0.2 7.00 ± 0.2 2.54 1.51



as possible to the power source terminal of the crystal product (between Vcc - GND).

Pin	Connection	
1, 2, 3, 6, 7, 10	N.C.	
4	GND	
5	OUT	
8	OE	
9	V_{CC}	

OE pin = "H" or "open": Specified frequency output. OE pin = "L" : Output is high impedance.

Pin	Connection
1	N.C
2	GND
3	OUT
4	V_{CC}

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►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

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(Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



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